

# Freeform Search

Database:

US Pre-Grant Publication Full-Text Database  
US Patents Full-Text Database  
US OCR Full-Text Database  
EPO Abstracts Database  
JPO Abstracts Database  
Derwent World Patents Index  
IBM Technical Disclosure Bulletins

Term:

Display:  Documents in Display Format:  Starting with Number

Generate: ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search

Clear

Interrupt

## Search History

DATE: Friday, October 01, 2004 [Printable Copy](#) [Create Case](#)

| <u>Set</u><br><u>Name</u><br>side by<br>side | <u>Query</u>   | <u>Hit</u><br><u>Count</u> | <u>Set</u><br><u>Name</u><br>result<br>set |
|--|--|----------------------------|--|
|  | DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR   |                            |  |
| <u>L14</u>                                   | L13 and l12  | 1                          | <u>L14</u>                                 |
| <u>L13</u>                                   | ((processor or control\$4 or cpu or host)near4 (determin\$3 or select\$3 or choos\$4 or chos\$4 or allow\$3)) with ((access\$3 or read\$4 or writ\$3 or transfer\$4) near4 (block or line))                  | 1915                       | <u>L13</u>                                 |
| <u>L12</u>                                   | L11 and l10 and l9   | 5                          | <u>L12</u>                                 |
| <u>L11</u>                                   | ((processor or control\$4 or cpu or host) with (partition\$4 or portion\$4 or section\$4))   | 550982                     | <u>L11</u>                                 |
| <u>L10</u>                                   | ((cache or block) near4 allocat\$4) with (processor or control\$4 or cpu or host))   | 556                        | <u>L10</u>                                 |
| <u>L9</u>                                    | ((multi or second or plurality or another) adj2 processor)   | 7773                       | <u>L9</u>                                  |
|  | DB=PGPB,USPT; PLUR=YES; OP=OR  |                            |  |
| <u>L8</u>                                    | L5 and l4  | 103                        | <u>L8</u>                                  |
| <u>L7</u>                                    | L6 and l4  | 3                          | <u>L7</u>                                  |
| <u>L6</u>                                    | ((processor or control\$4 or cpu or host)near4 (determin\$3 or select\$3 or choos\$4 or chos\$4 or allow\$3)) with ((access\$3 or read\$4 or writ\$3 or transfer\$4) near4 (alloca\$4 adj3 (block or line))) | 10                         | <u>L6</u>                                  |
| <u>L5</u>                                    | ((processor or control\$4 or cpu or host)near4 (determin\$3 or select\$3 or choos\$4 or chos\$4 or allow\$3)) with ((access\$3 or read\$4 or writ\$3 or transfer\$4) near4 (block or line))                  | 8911                       | <u>L5</u>                                  |
| <u>L4</u>                                    | L3 and l2 and l1   | 494                        | <u>L4</u>                                  |
| <u>L3</u>                                    | ((processor or control\$4 or cpu or host) with (partition\$4 or portion\$4 or section\$4))   | 562903                     | <u>L3</u>                                  |
| <u>L2</u>                                    | ((cache or block) near4 allocat\$4) with (processor or control\$4 or cpu or host))   | 3082                       | <u>L2</u>                                  |